Day : Tuesday
Date: 9/25/2007

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Inventor Name Search Result

Your Search was:

Last Name = WEGO
First Name = ARILD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10000038	7058073	150	12/04/2001	ARRANGEMENT AND METHOD FOR TRANSMITTING DATA OVER A TDM BUS	WEGO, ARILD
10497858	Not Issued	41	06/07/2004	Rate adaption within a tdm switch using bit stuffing	WEGO, ARILD
10500048	Not Issued	41	06/25/2004	Method for reducing the amount of needed memory in a tdm switch system	WEGO, ARILD
10521388	7155191	150	01/14/2005	METHOD AND ARRANGEMENT FOR REDUCING PHASE JUMPS WHEN SWITCHING BETWEEN SYNCHRONISATION SOURCES	WEGO, ARILD
10526526	Not Issued	30	03/02/2005	Switching arrangement including time-slot buses and serveral buffers	WEGO, ARILD
10538722	Not Issued	30	06/13/2005	Method for reducing the bus load in a synchronous data bus system	WEGO, ARILD

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = HELLUM
First Name = PAL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08929463	6104729	150	09/15/1997	METHOD AND APPARATUS FOR SYNCHRONIZATION OF TIME STAMPING	HELLUM, PAL
09319415	Not Issued	161	09/02/1999	BROADCASTING IN AN ATM-NETWORK	HELLUM, PAL
09331326	6650449	150	09/21/1999	METHOD AND DEVICE FOR NETWORK PROTECTION	HELLUM, PAL LANGVA
09369306	6757295	150	08/02/1999	METHOD FOR REGENERATING A POINT-TO-MULTIPOINT INTERFACE ON A POINT-TO-POINT INTERFACE	HELLUM, PAL LONG
09614249	6742063	150	07/12/2000	METHOD AND APPARATUS FOR EFFICIENT TRANSFER OF DATA PACKETS	HELLUM, PAL LONGVA
09711609	6433633	150	11/13/2000	AUTOMATIC GAIN CONTROL SYSTEM WITH SIGNAL PRESENCE CONDITIONING	HELLUM, PAL LONGVA
10497858	Not Issued	41	06/07/2004	Rate adaption within a tdm switch using bit stuffing	HELLUM, PAL LONGVA
10500048	Not Issued	41	06/25/2004	Method for reducing the amount of needed memory in a tdm switch system	HELLUM, PAL LONGVA
10521388	7155191	150	01/14/2005	METHOD AND ARRANGEMENT FOR REDUCING PHASE JUMPS WHEN SWITCHING BETWEEN SYNCHRONISATION SOURCES	HELLUM, PAL LONGVA
10526526	Not Issued	30	03/02/2005	Switching arrangement including time-slot buses and serveral buffers	HELLUM, PAL LONGVA
10538722	Not	30	06/13/2005	Method for reducing the bus load	HELLUM, PAL

	Issued			in a synchronous data bus system	LONGVA
10595136	Not Issued	17	08/06/2007	High Availability System Based on Separated Control and Traffic System	HELLUM, PAL LONGVA
09331340	6438698	150	08/13/1999	REDUCTION OF ELECTROMAGNETIC EMISSION	HELLUM, PAL LONGVA

Inventor Search Completed: No Records to Display.

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